1. SOFTWARE TESTING METHODOLOGIES

By the give up of the path, the scholar need to:

• Have a capability to use software program trying out knowledge and engineering strategies.

• Have a capability to design and behavior a software program take a look at procedure for a software trying out venture.

• Have a capacity to become aware of the desires of software program test automation, and outline and increase a check device to support test automation.

• Have a potential understand and perceive various software checking out problems, and solve those problems through designing and selecting software program check fashions, criteria, techniques, and methods.

• Have a potential to use diverse communique methods and skills to talk with their teammates to behavior their exercise-orientated software checking out projects.

• Have primary know-how and information of contemporary troubles in software program checking out, inclusive of component based software testing troubles

• Have an ability to use software program testing techniques and modern software testing gear for his or her testing projects.

2. OBJECT ORIENTED ANALYSIS DESIGN

• Graduate can able to take inside the case research and version it in notable perspectives with appreciate customer requirement together with use case, logical, element and deployment and many others, and education of record of the assignment for the unified library software program
• Offer a picture of the targeted nation of a gadget at a point in time the usage of an uml (unified modeling language) item diagram.
• Understand while to use generalization, aggregation, and composition relationships.
• Specify exceptional forms of business policies in a category diagram.

3. MOBILE APPLICATION DEVELOPMENT

Outcomes:

• Ability to evaluate and pick out suitable solutions to the cellular computing platform.

• Potential to broaden the consumer interface.

• Capacity design easy mobile cellphone recreation.

4. VLSI DESIGN

Upon efficiently finishing the route, the student must be capable of:

• Gather qualitative understanding about the fabrication process of integrated circuit using mos transistors.

• Pick an appropriate inverter depending on specs required for a circuit

• Draw the layout of any good judgment circuit which facilitates to apprehend and estimate parasitic of any logic circuit

• Layout special forms of common sense gates using cmos inverter and examine their transfer characteristics.

• Offer layout standards required to layout building blocks of data route the usage of gates.

• Layout simple reminiscences using mos transistors and might understand layout of huge recollections.

• Design easy good judgment circuit the use of pla, buddy, fpga and cpld.

• Understand distinctive kinds of faults that may occur in a machine and research the concept of trying out and adding greater hardware to enhance testability of device.

5. SEMANTIC WEB AND SOCIAL NETWORK

Outcomes:

•Ability to apprehend and knowledge representation for the semantic web.

•Ability to create ontology.

•Capacity to build a blogs and social networks

6. INFORMATION RETRIVAL SYSTEM

Outcomes: Become aware of primary theories in facts retrieval systems.

• Identify the evaluation gear as they apply to statistics retrieval systems

• Is familiar with the problems solved in contemporary ir structures.

• Describes the blessings of present day INFORMATION RETRIVAL systems

• Recognize the problem of representing and retrieving files.

7. CT & ST LAB

Outcomes:

• Capability to apprehend the history, fee of the use of and building case tools.

• Capability to construct examine hybrid case gear by using integrating existing tools.

• Potential to deliver the product with qualitative

8. MAD LAB

Outcomes:

• Capacity to put in J2ME toolkit.

- Ability to increase the person interface and authenticate with an internet server.
- Potential to design web utility the usage of J2ME.